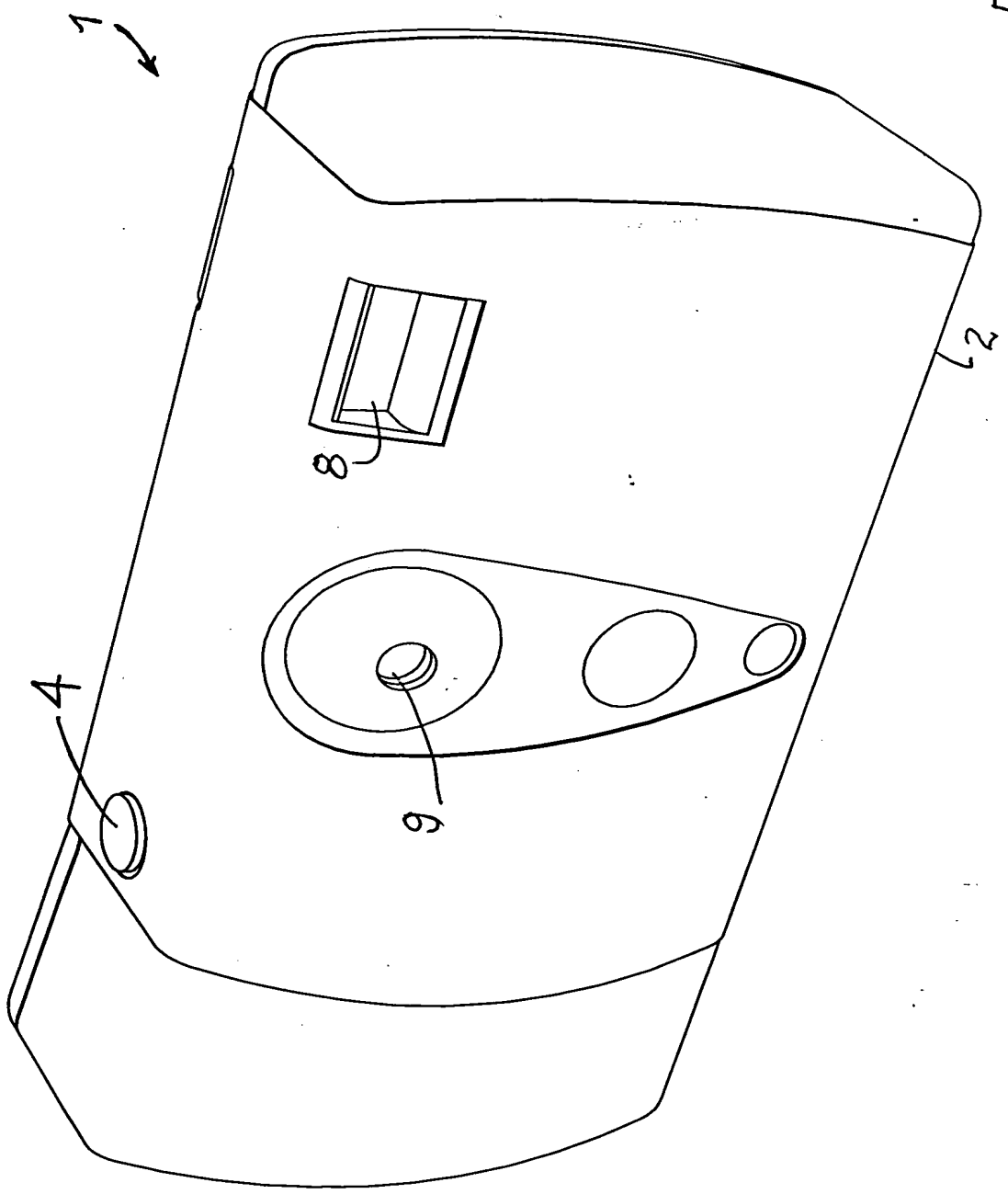
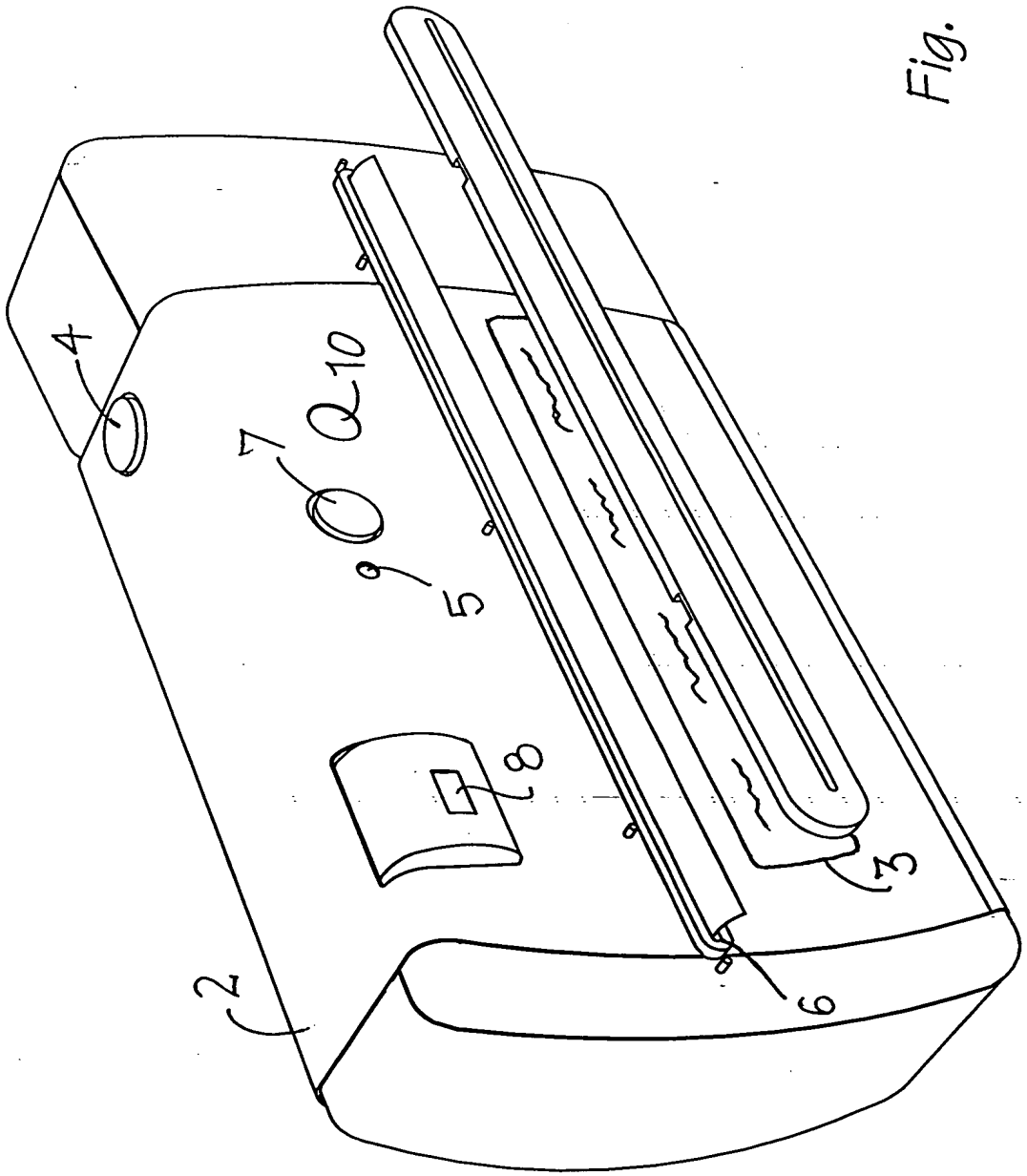


Fig. 1



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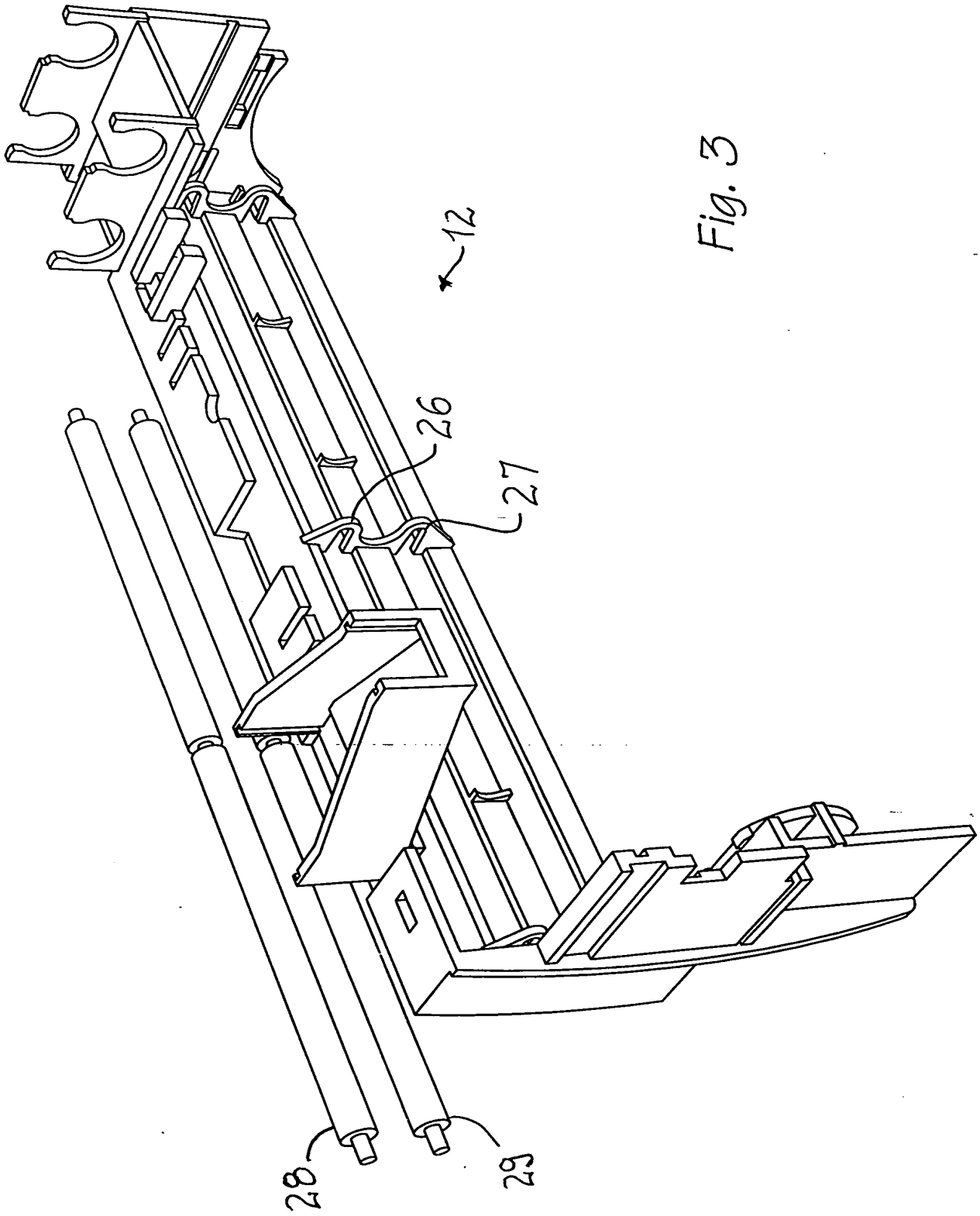

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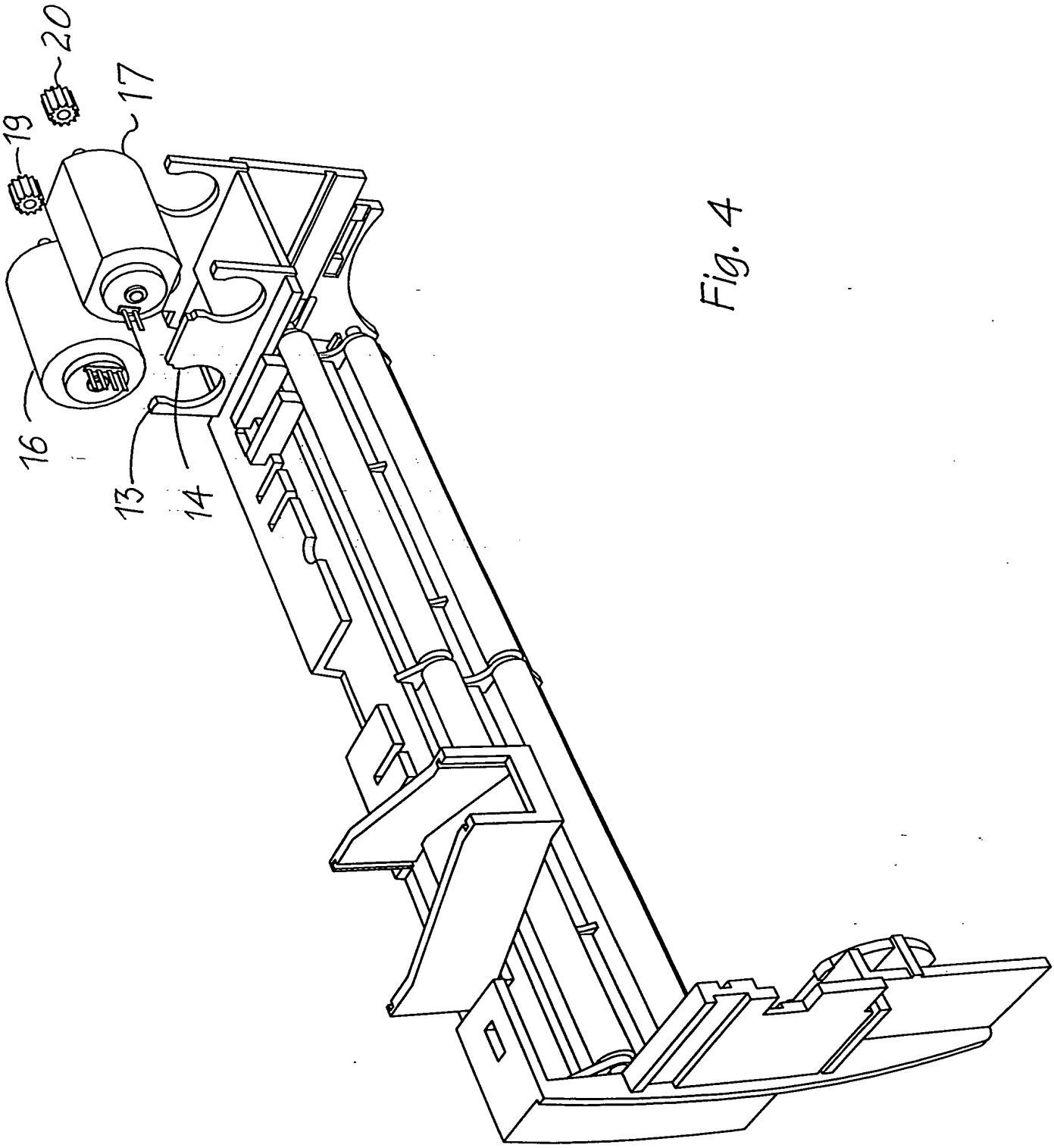

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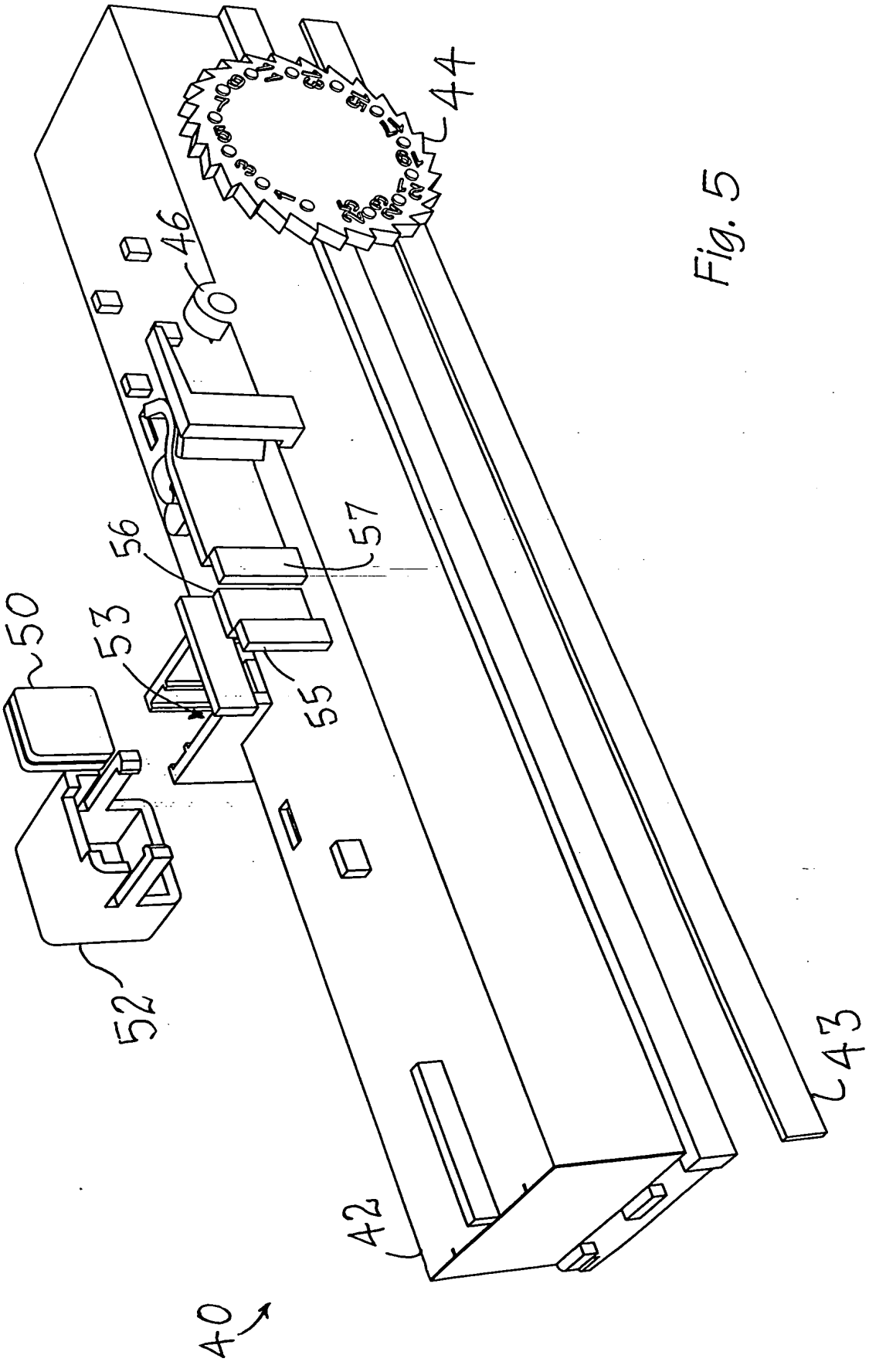

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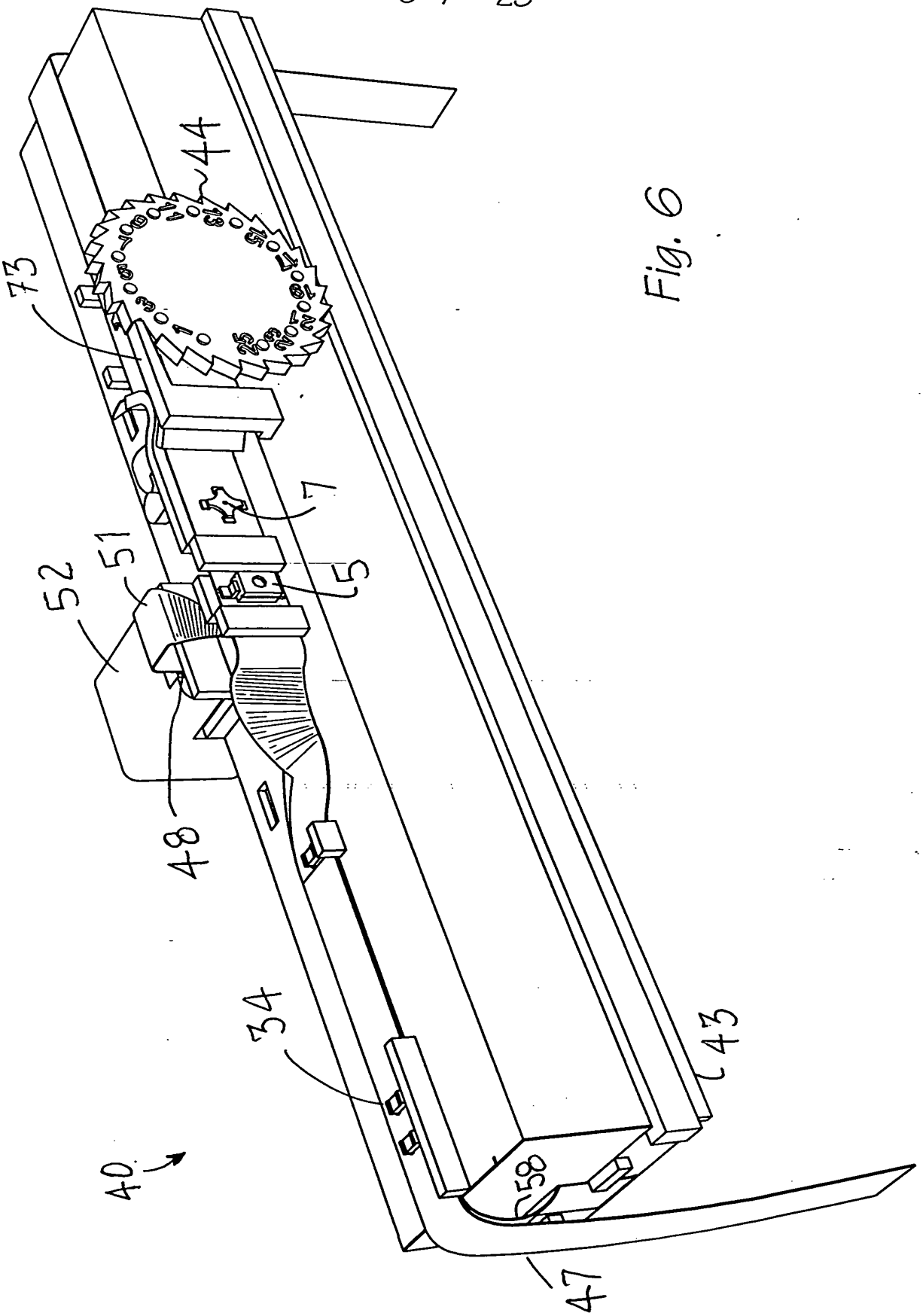

Fig. 6

EP 0 607 454 A1

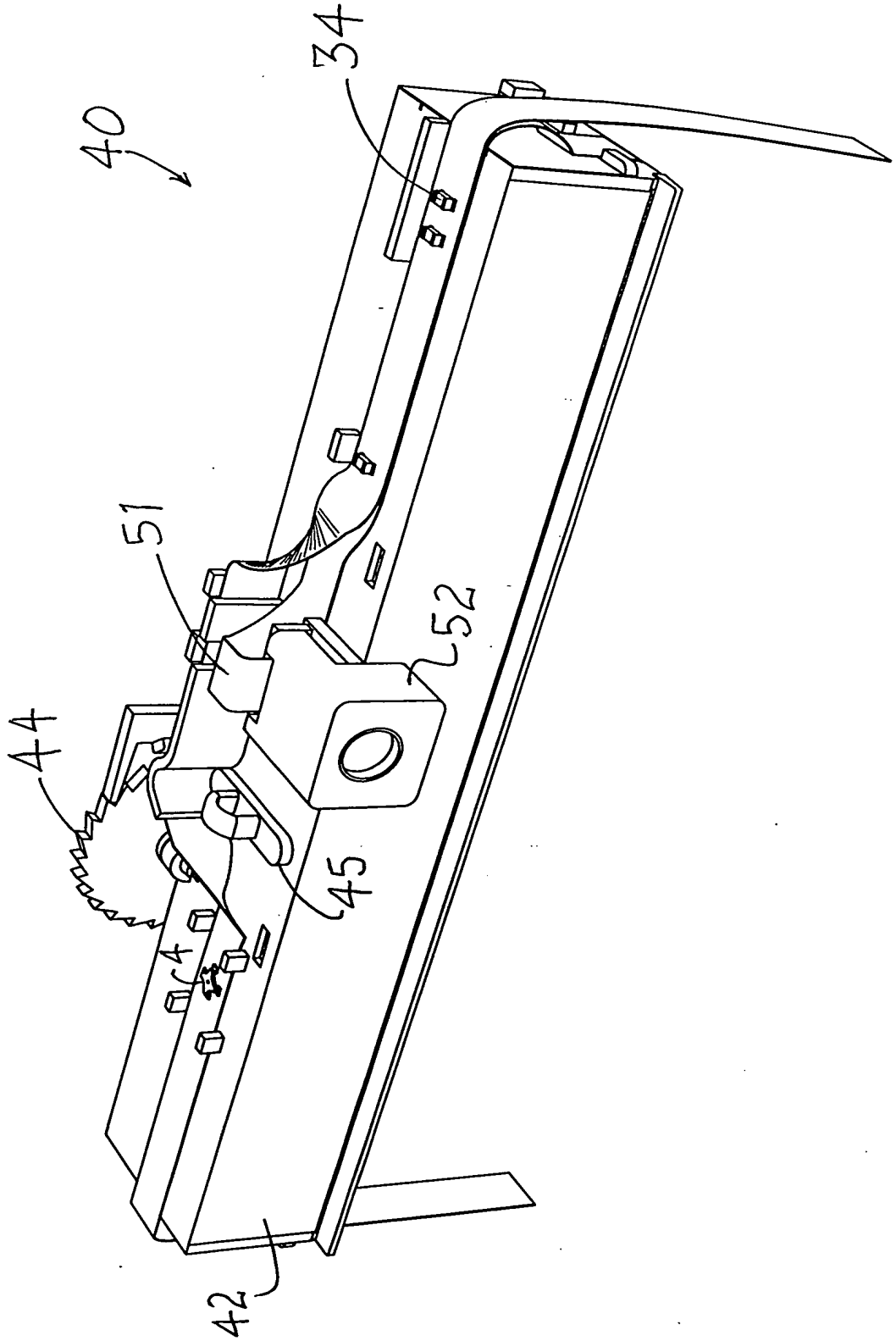

Fig. 7

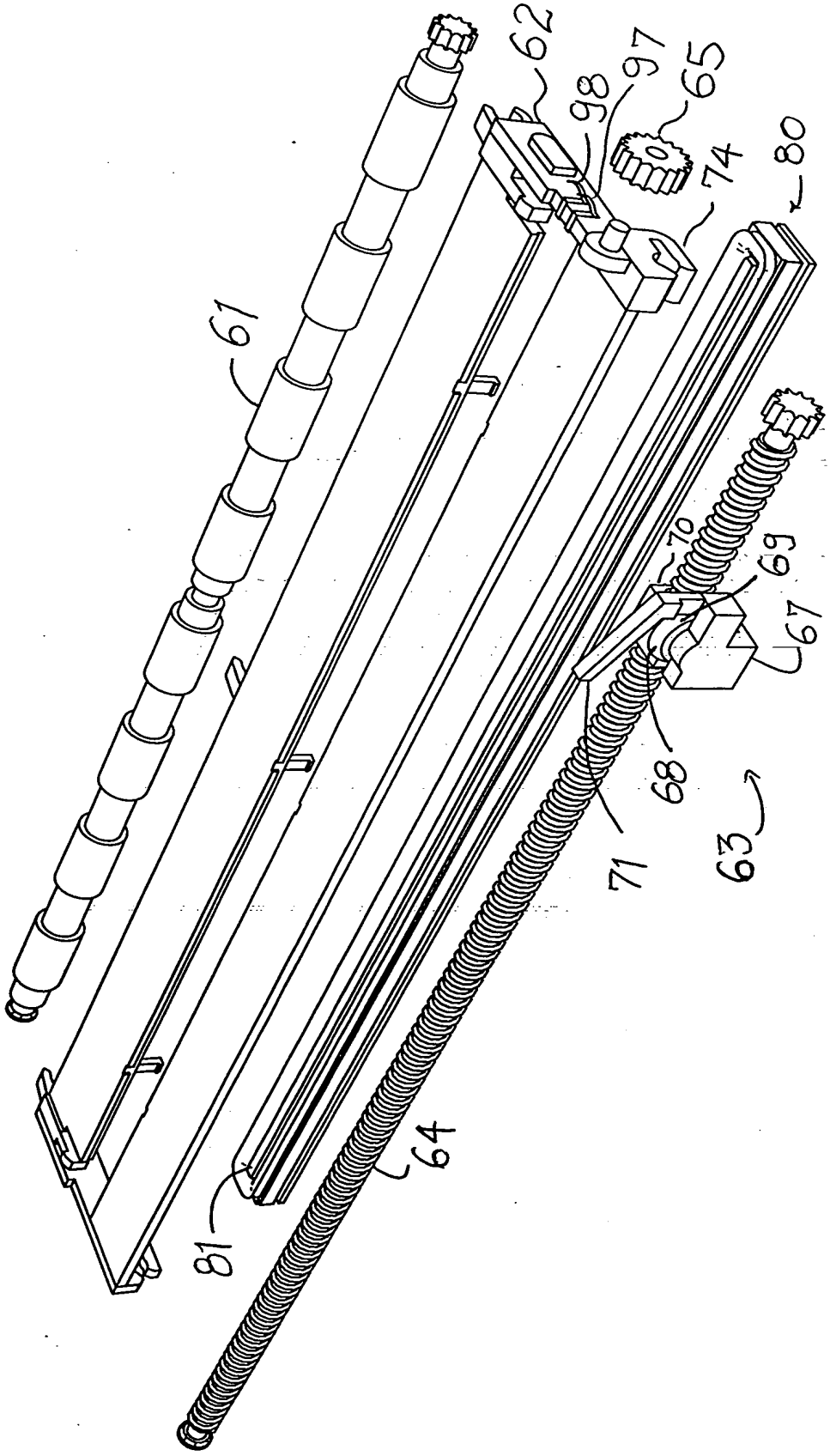

Fig. 8

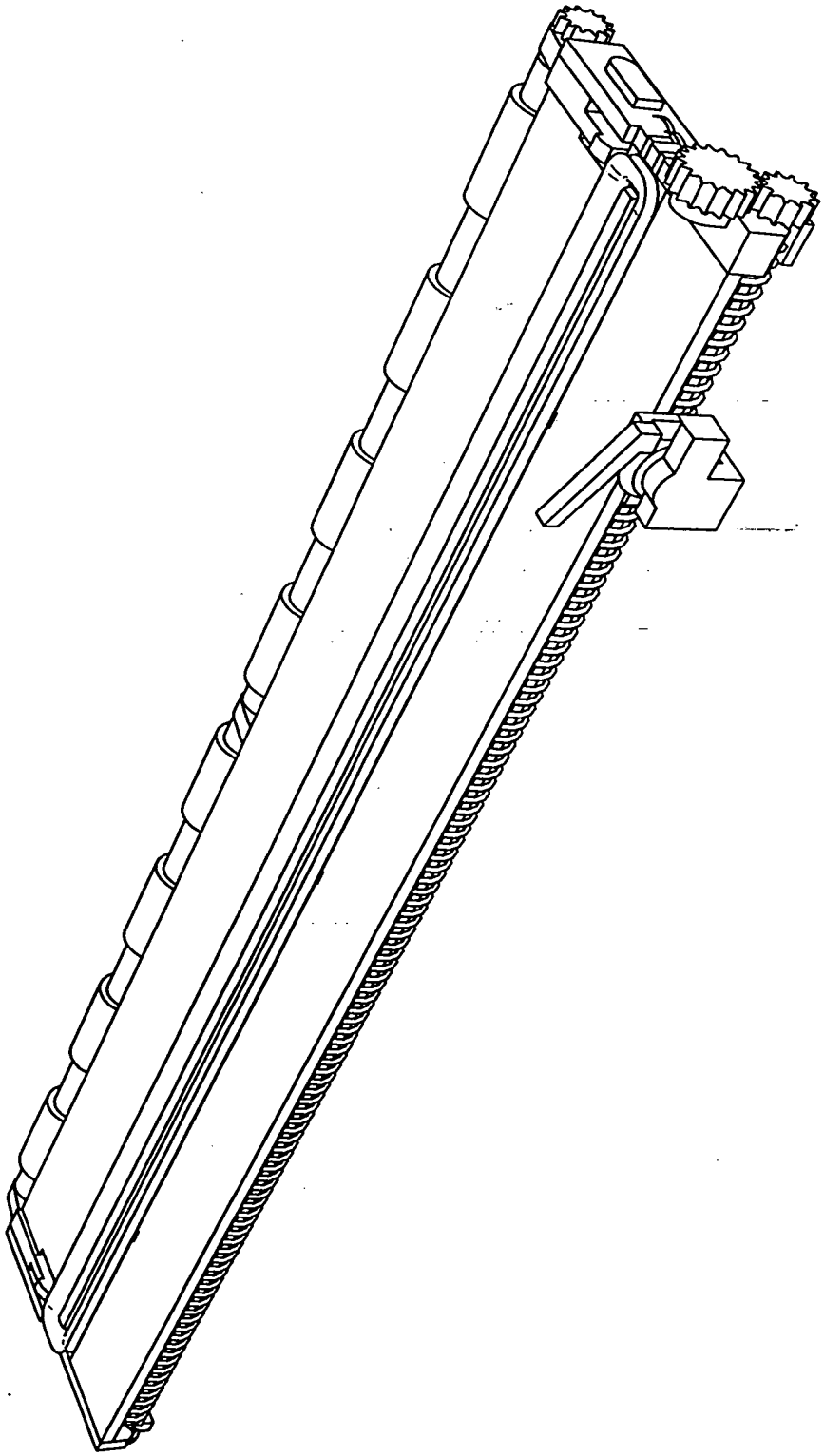

Fig. 9

EP 0 974 269 A2

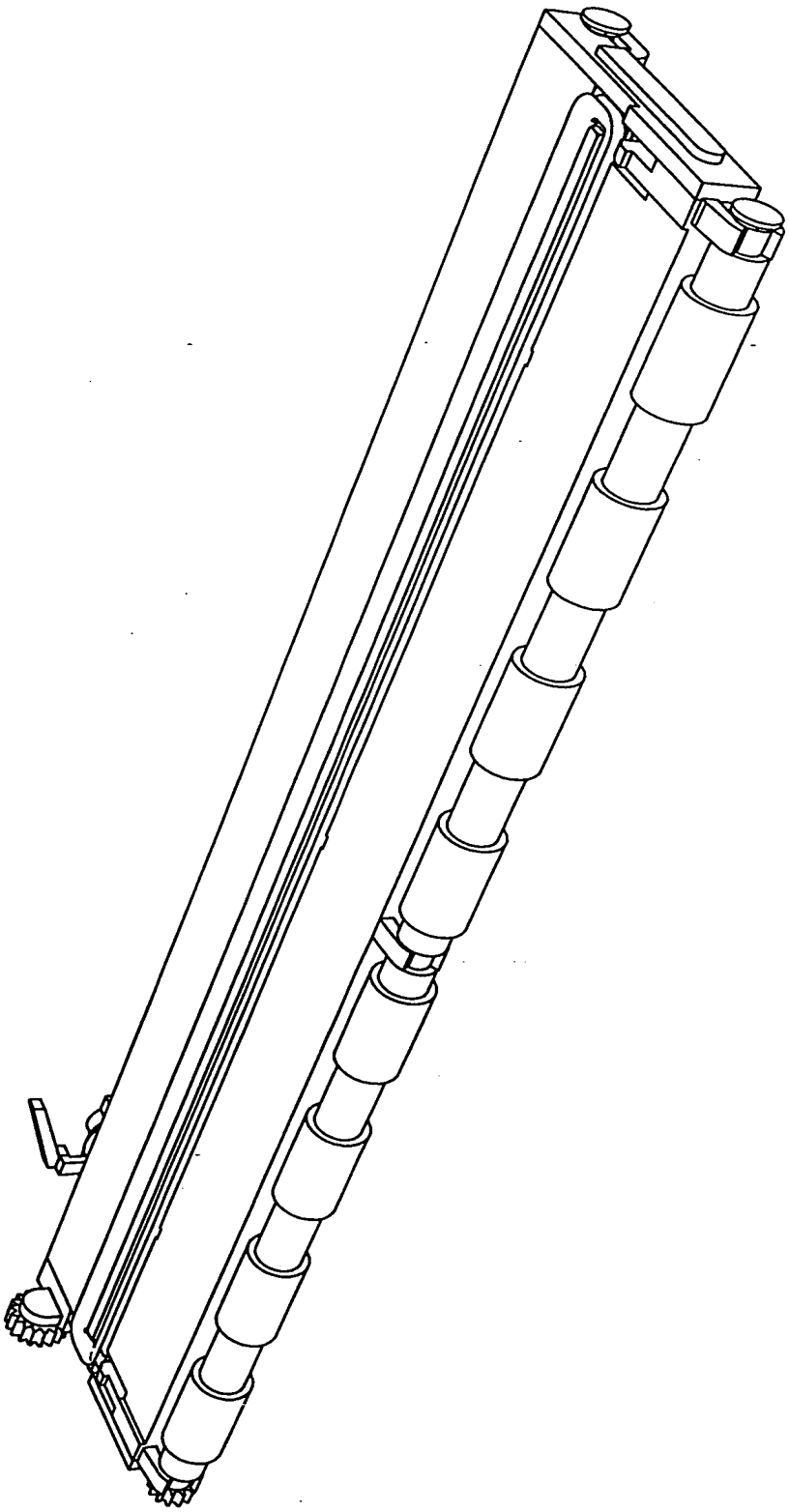

Fig. 10

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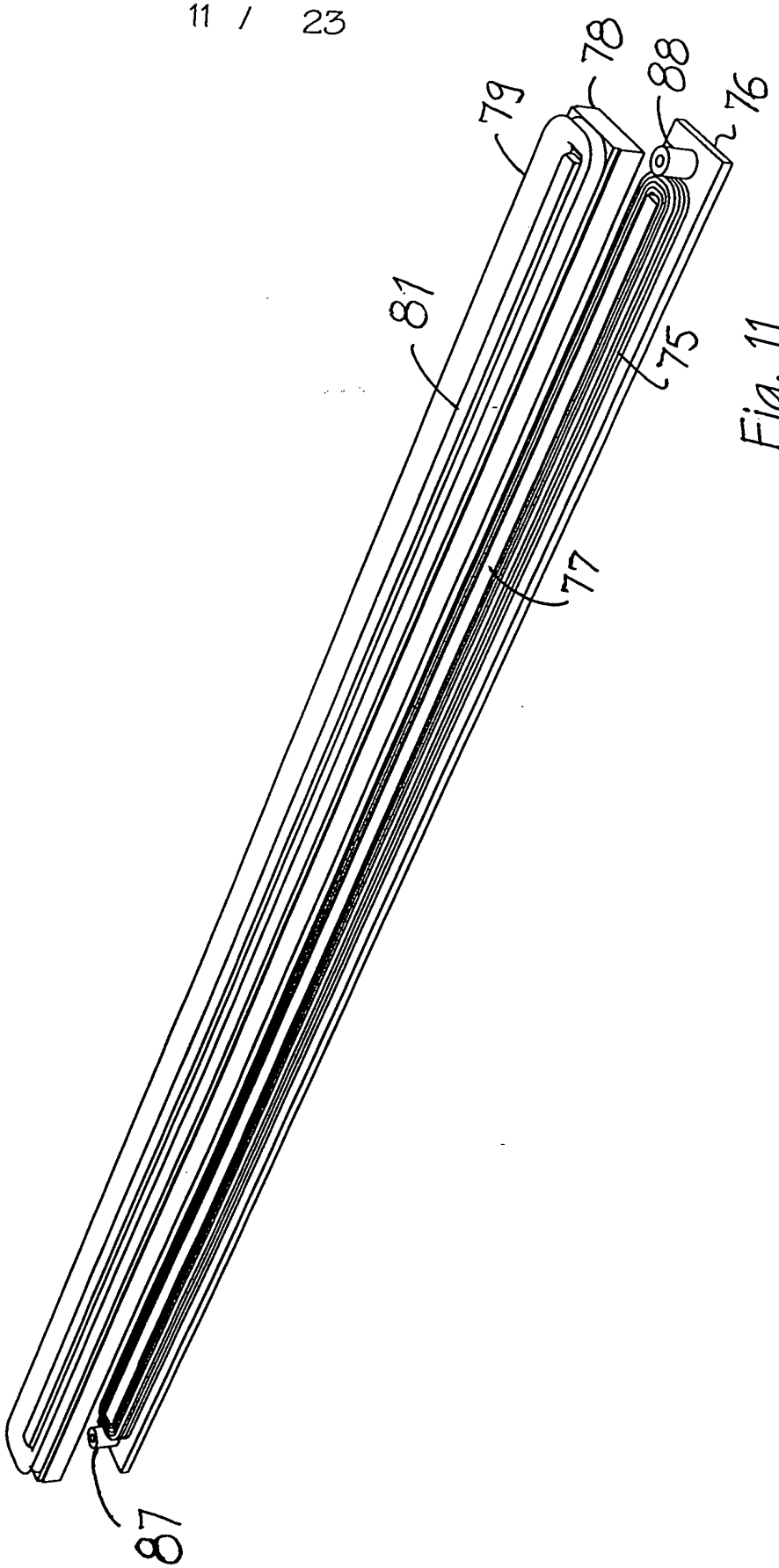


Fig. 11

EP 0 774 260 A2

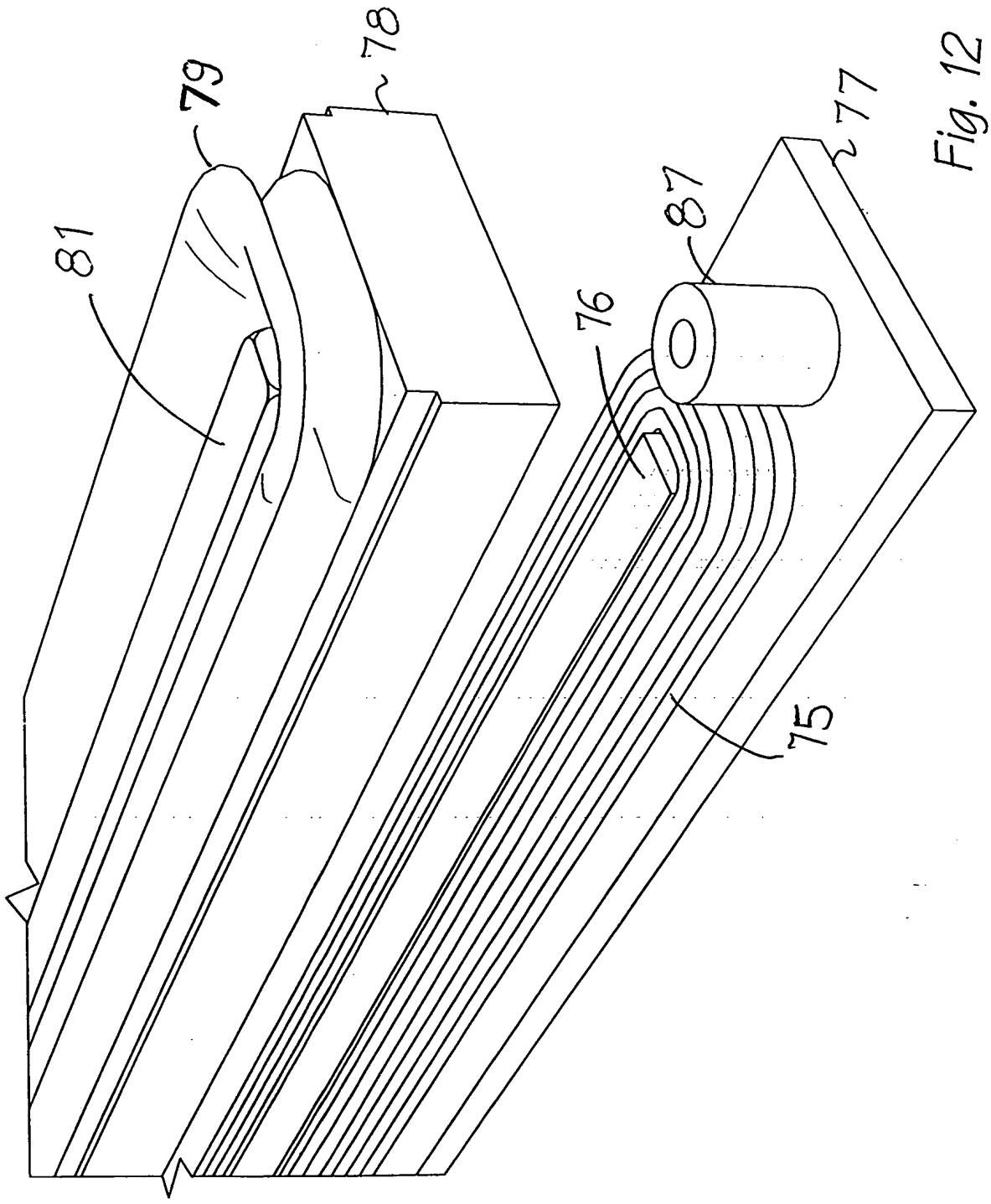


Fig. 12

BOOT SEQUENCE

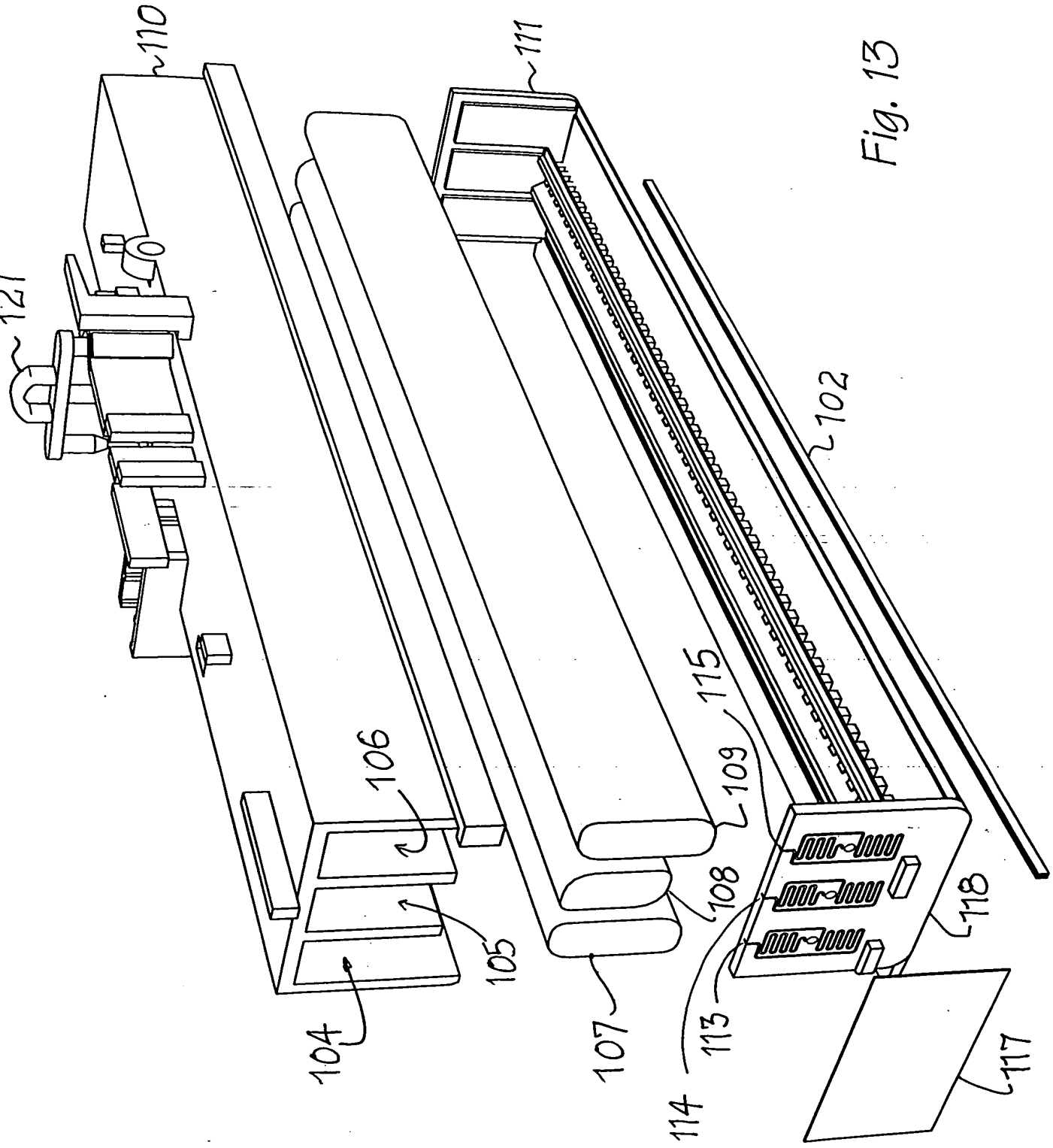


Fig. 13

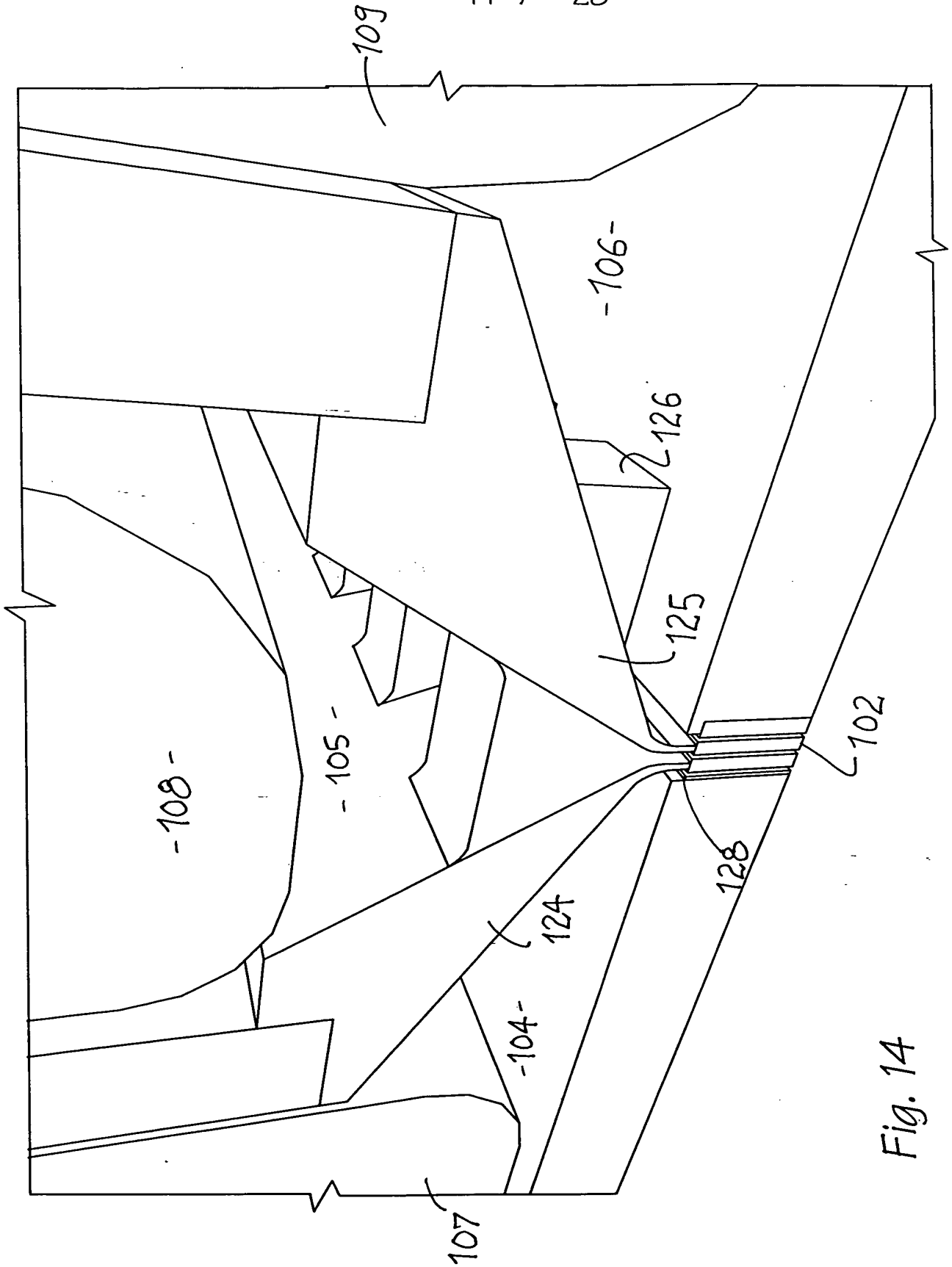
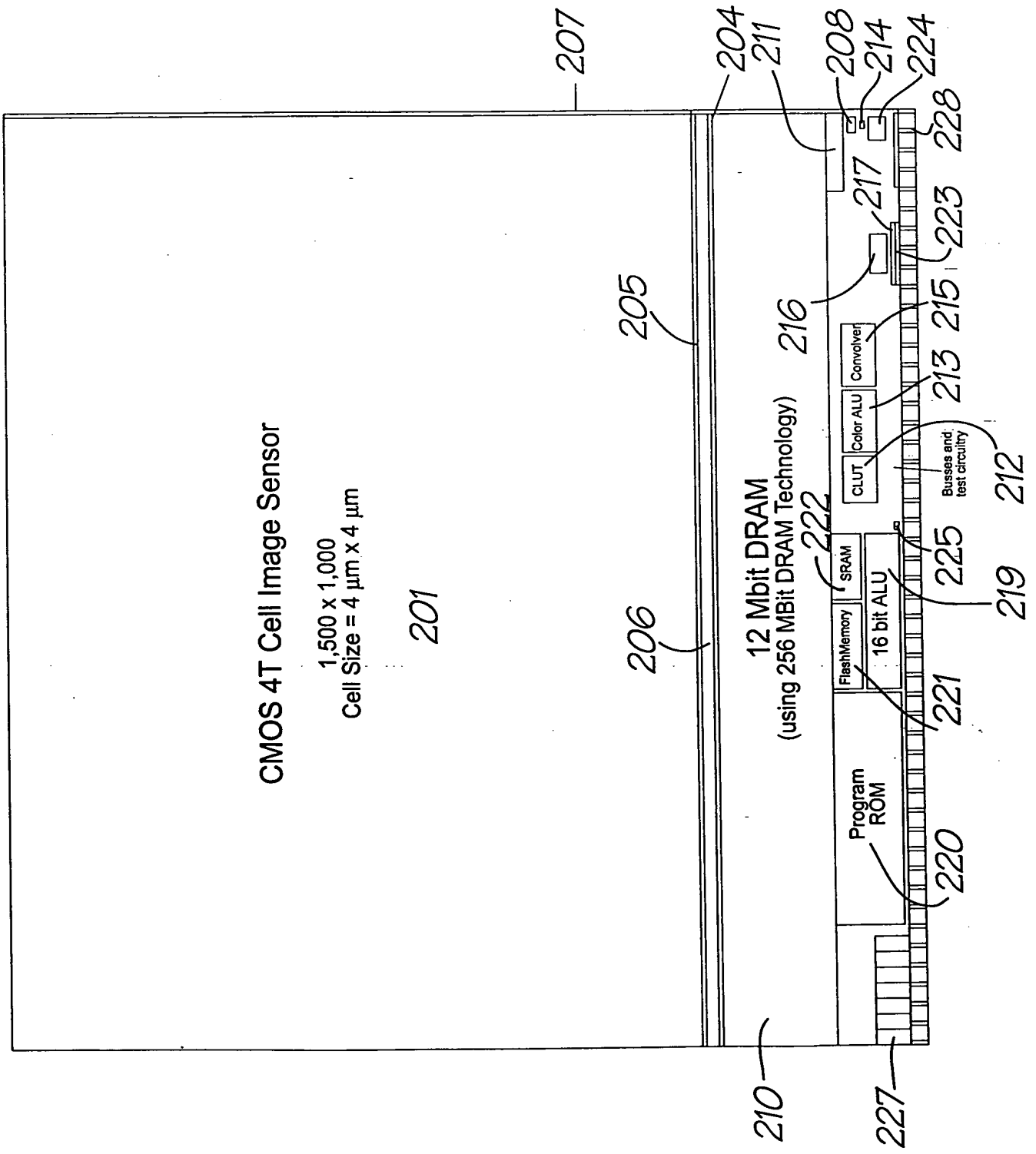


Fig. 14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1,500 x 1,000
Cell Size = 4 µm x 4 µm
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12 Mbit DRAM
(using 256 MBit DRAM Technology)
Convolver
Color ALU
CLUT
Busses and test circuitry
SRAM
FlashMemory
16 bit ALU
Program ROM
207
204
211
208
214
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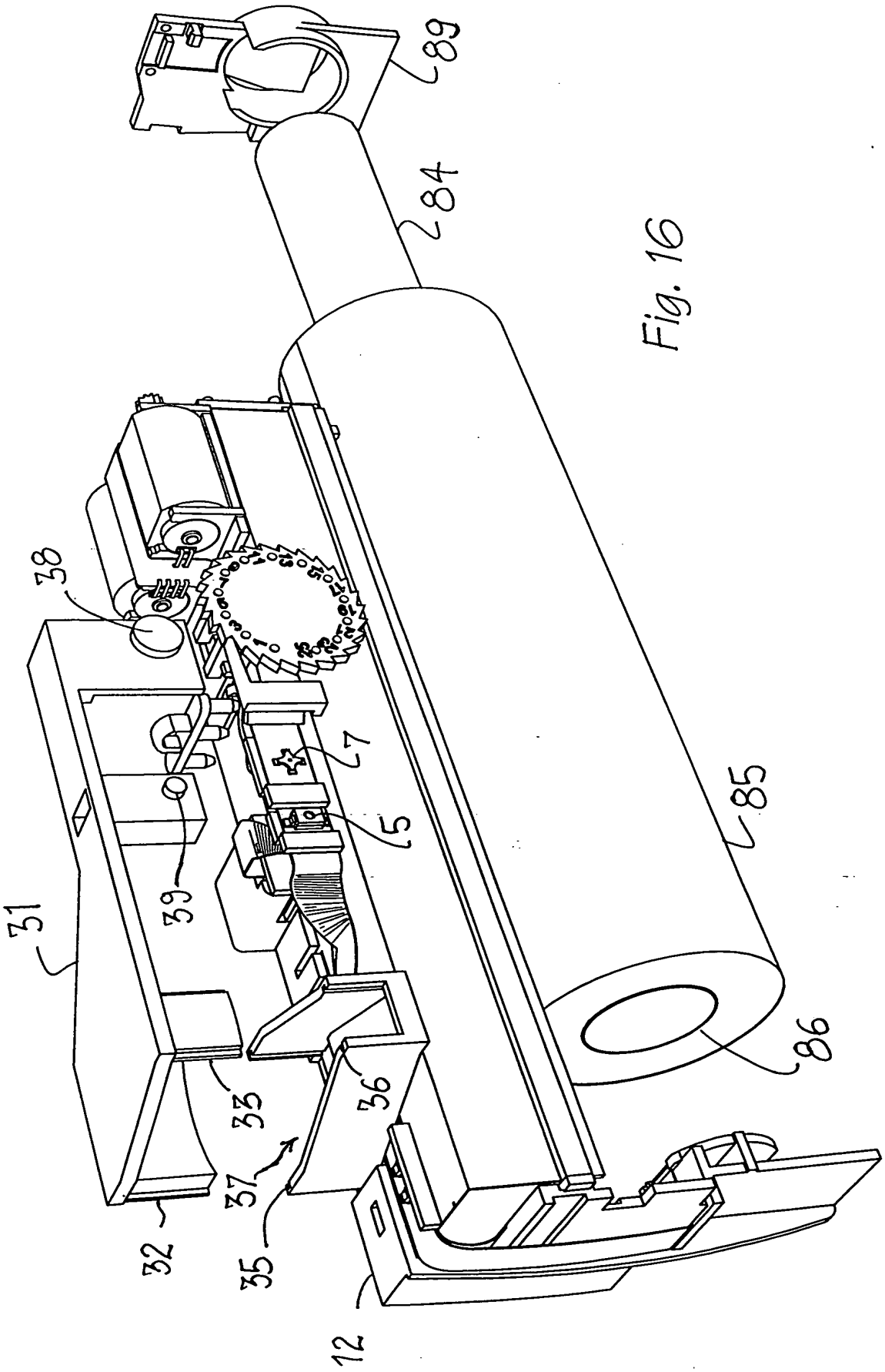
Fig. 15



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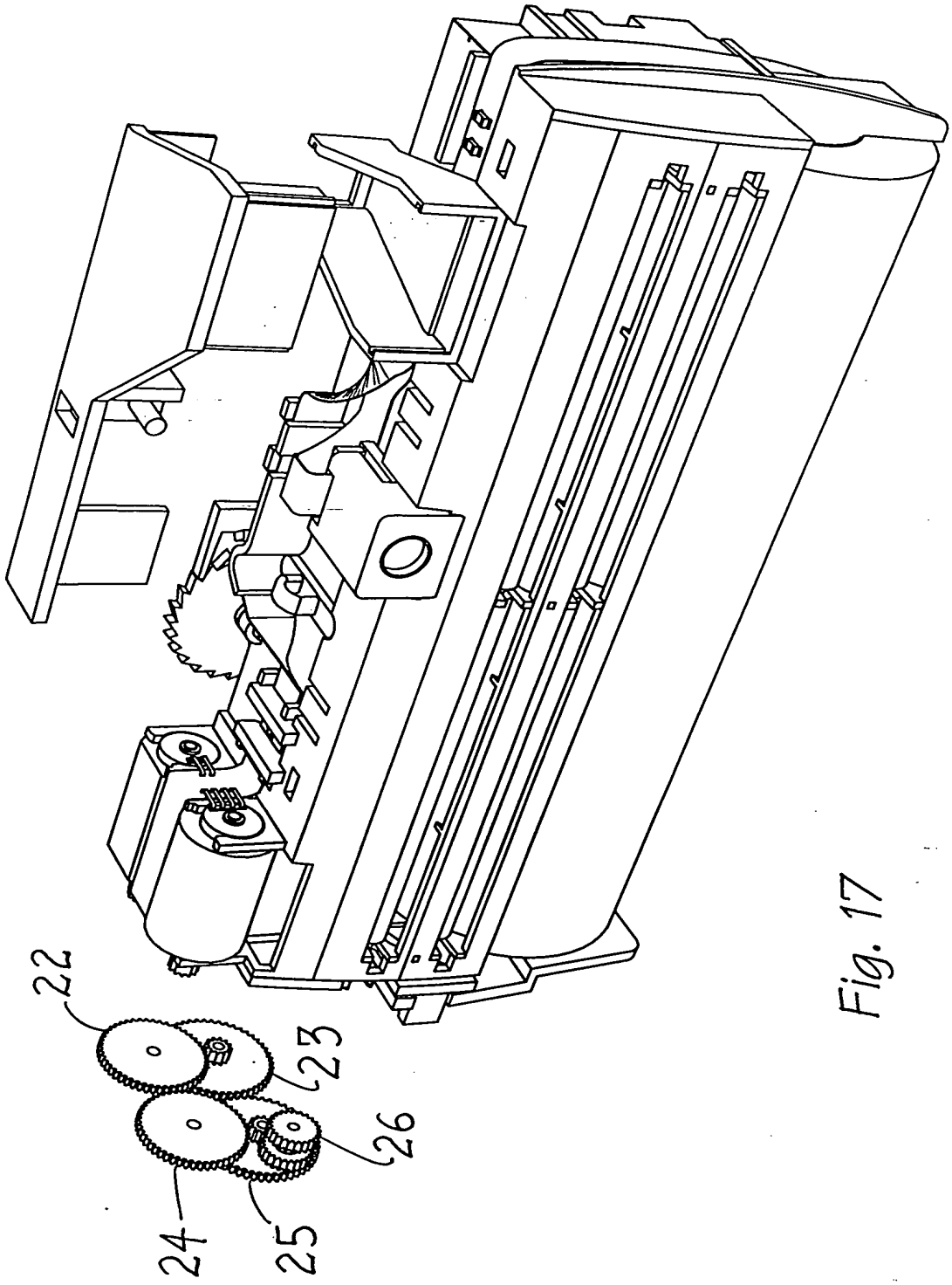
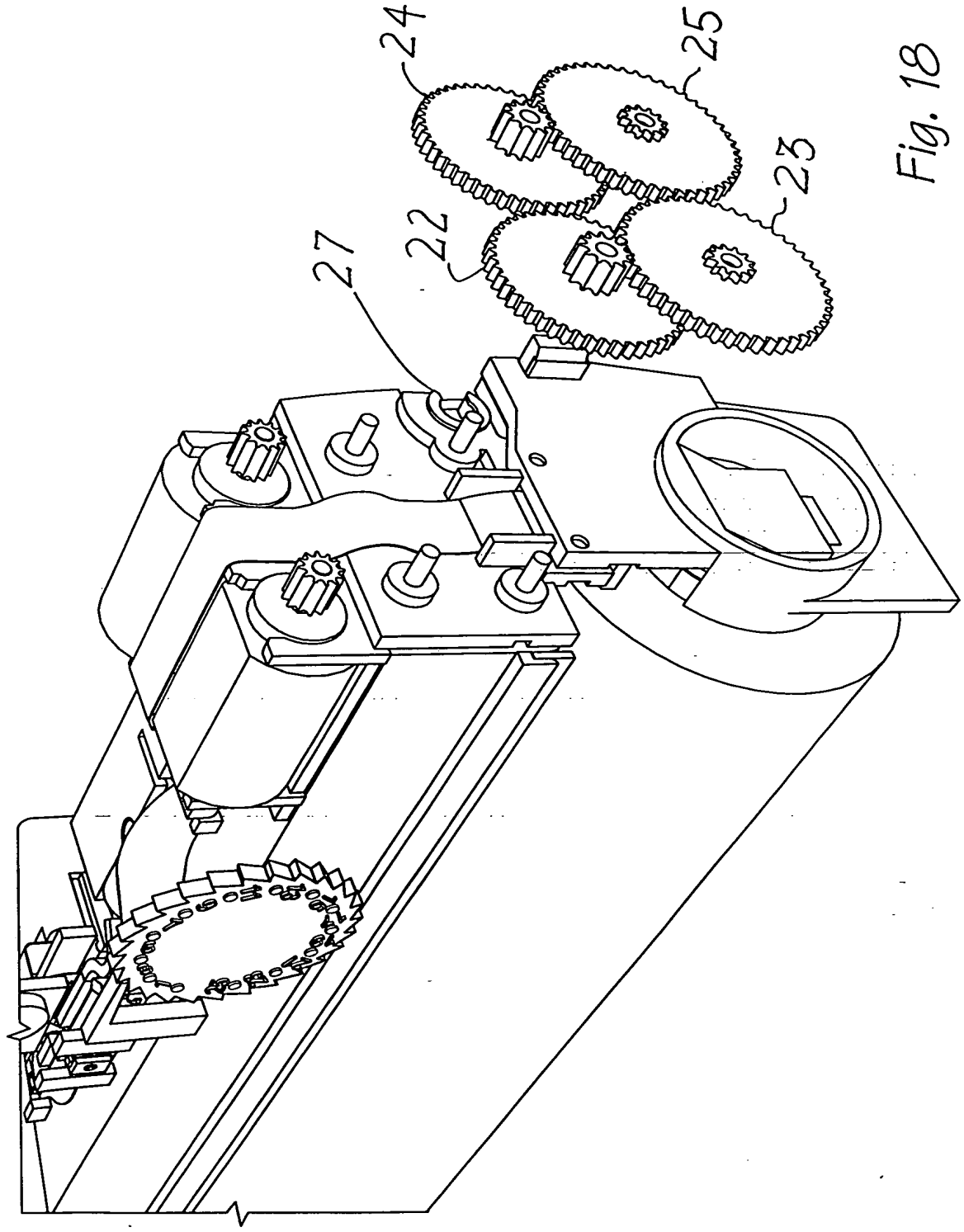


Fig. 17



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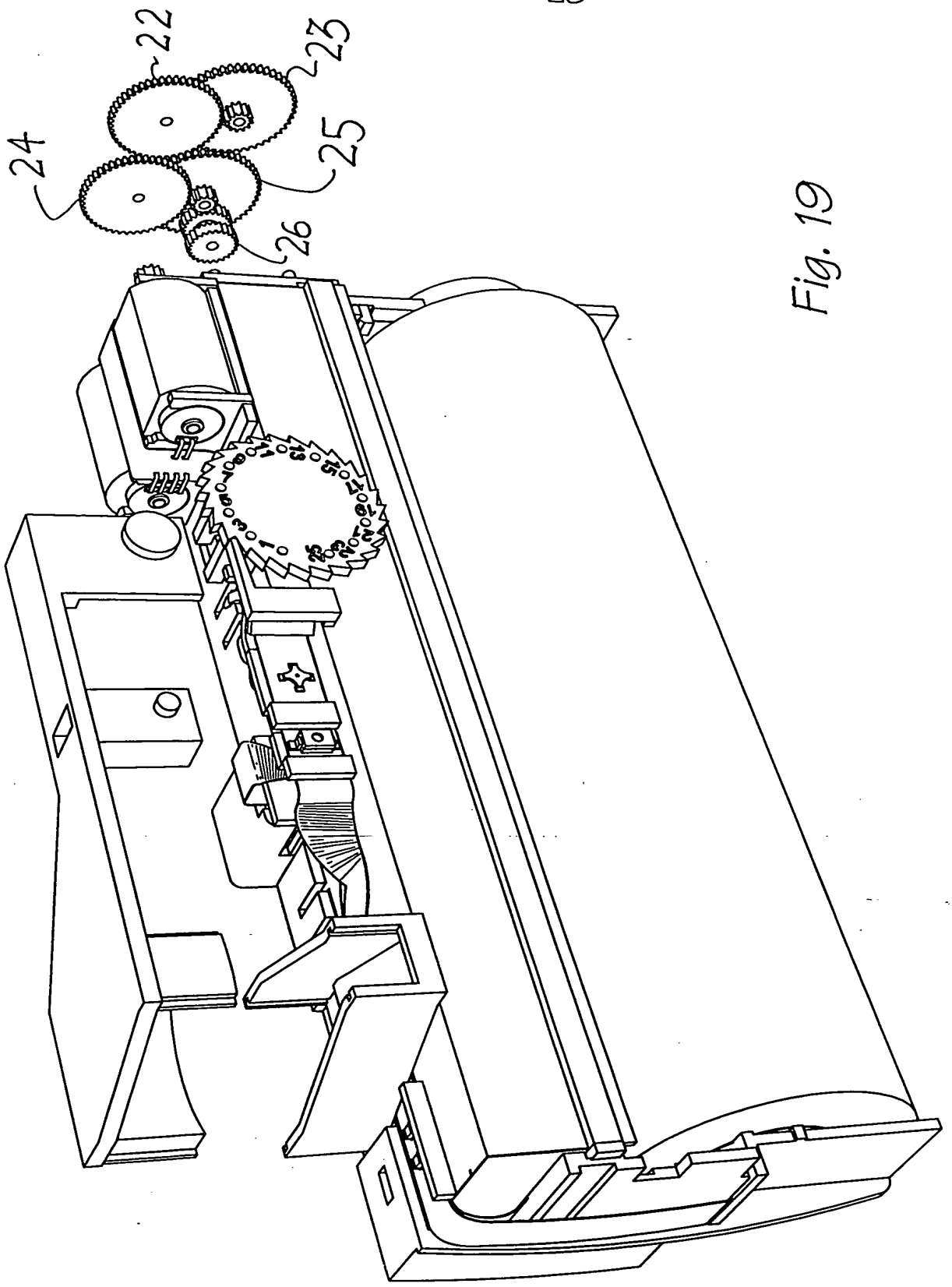

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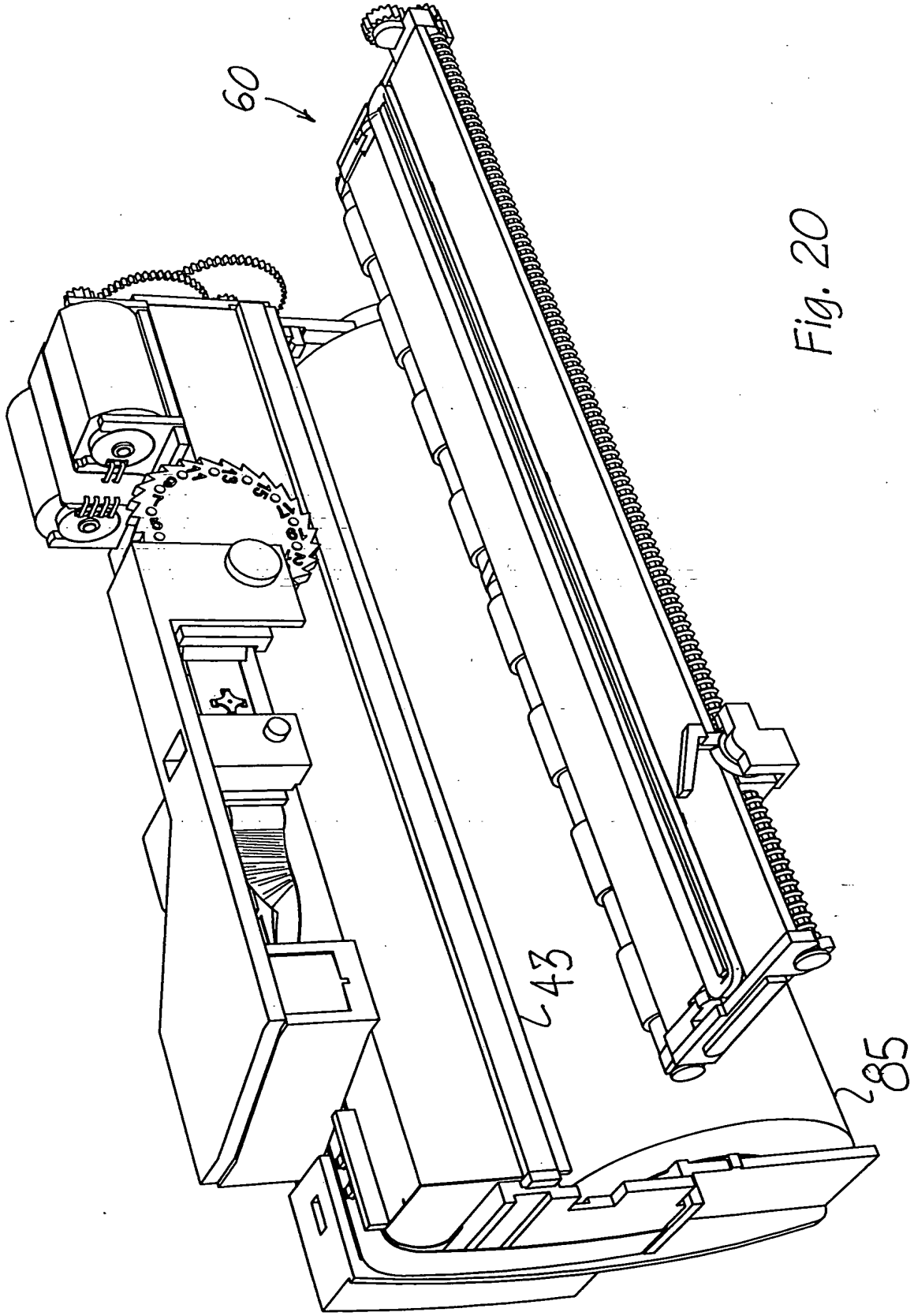


Fig. 20

BSDOCID: <4527560A1>

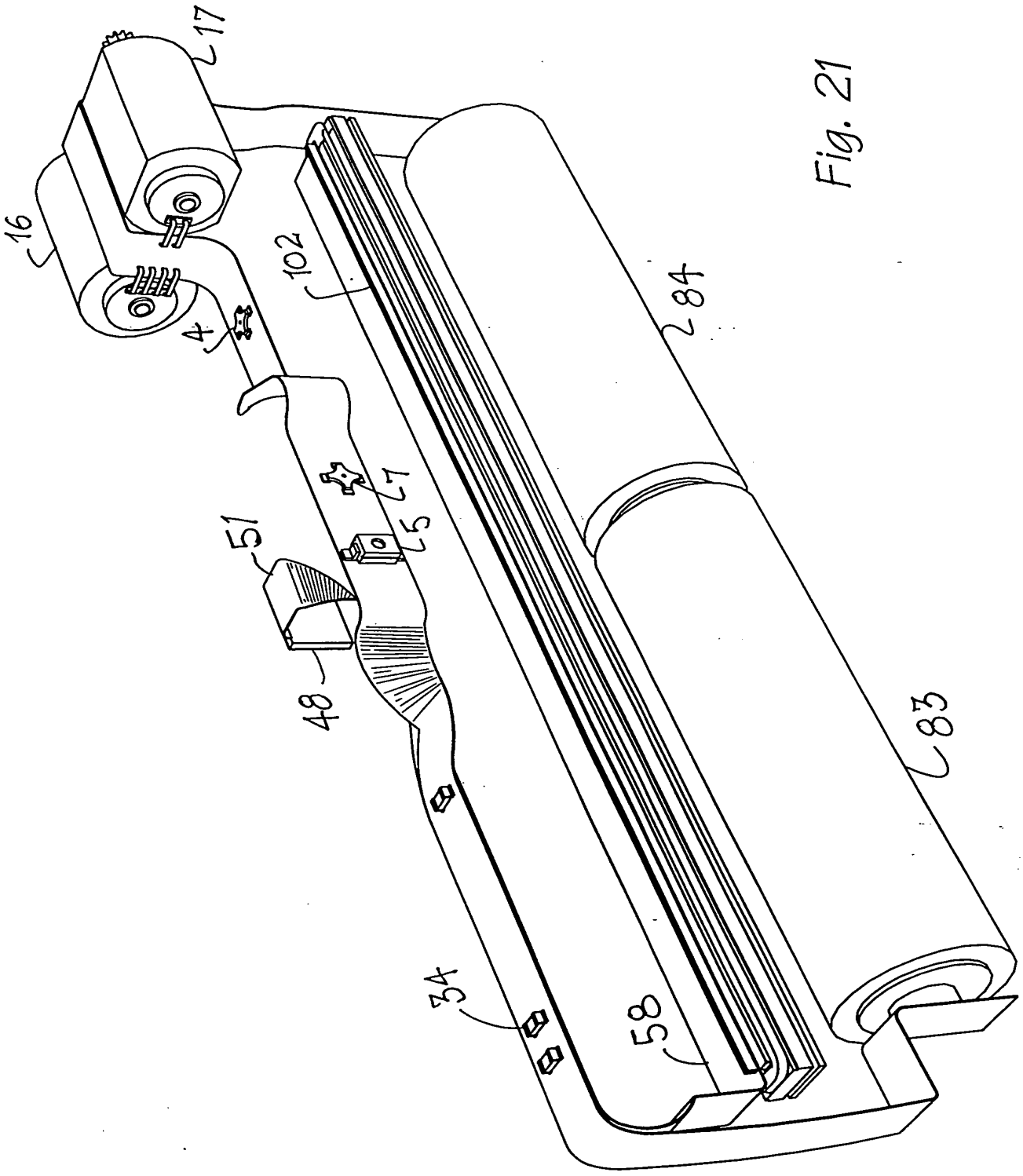
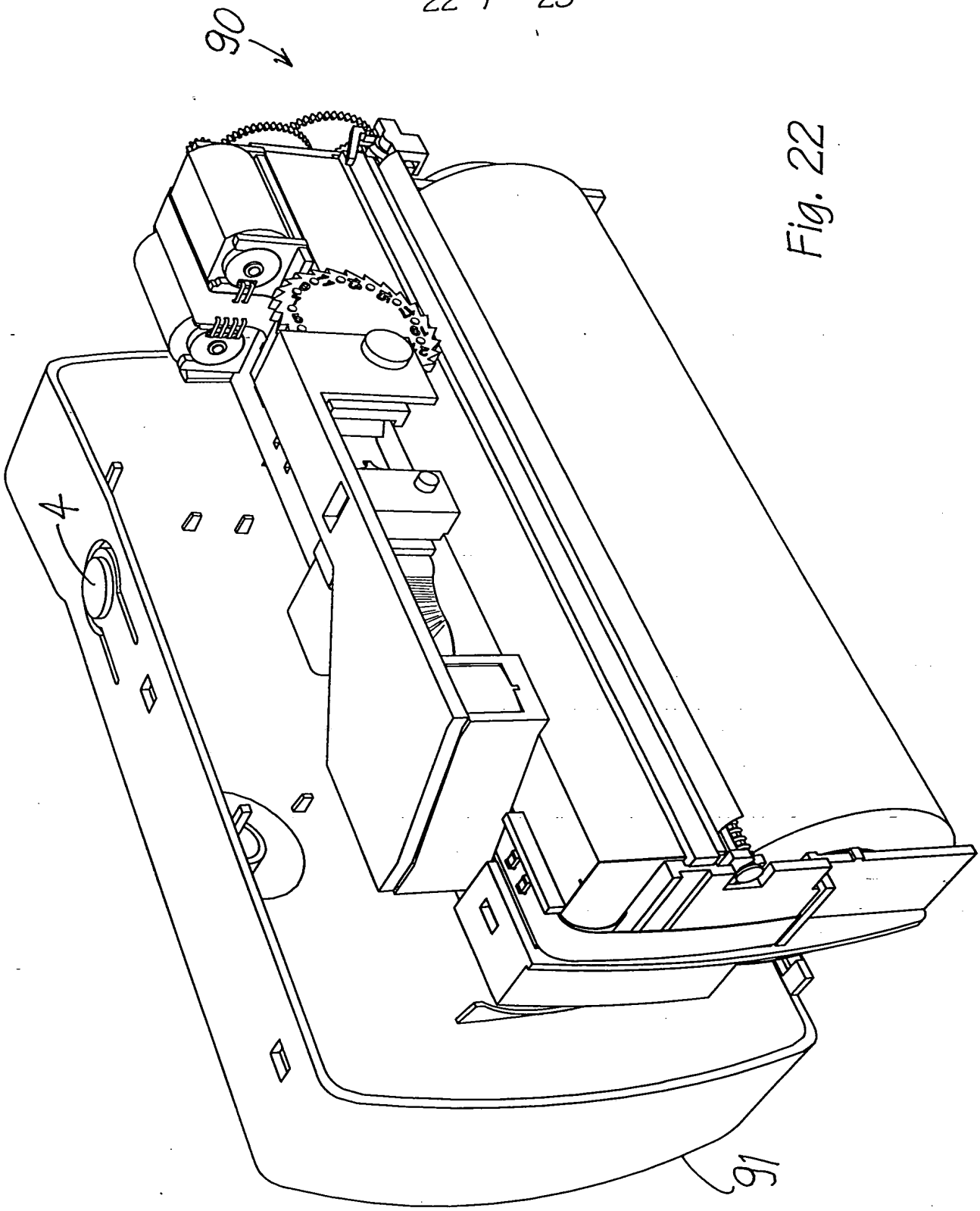


Fig. 21



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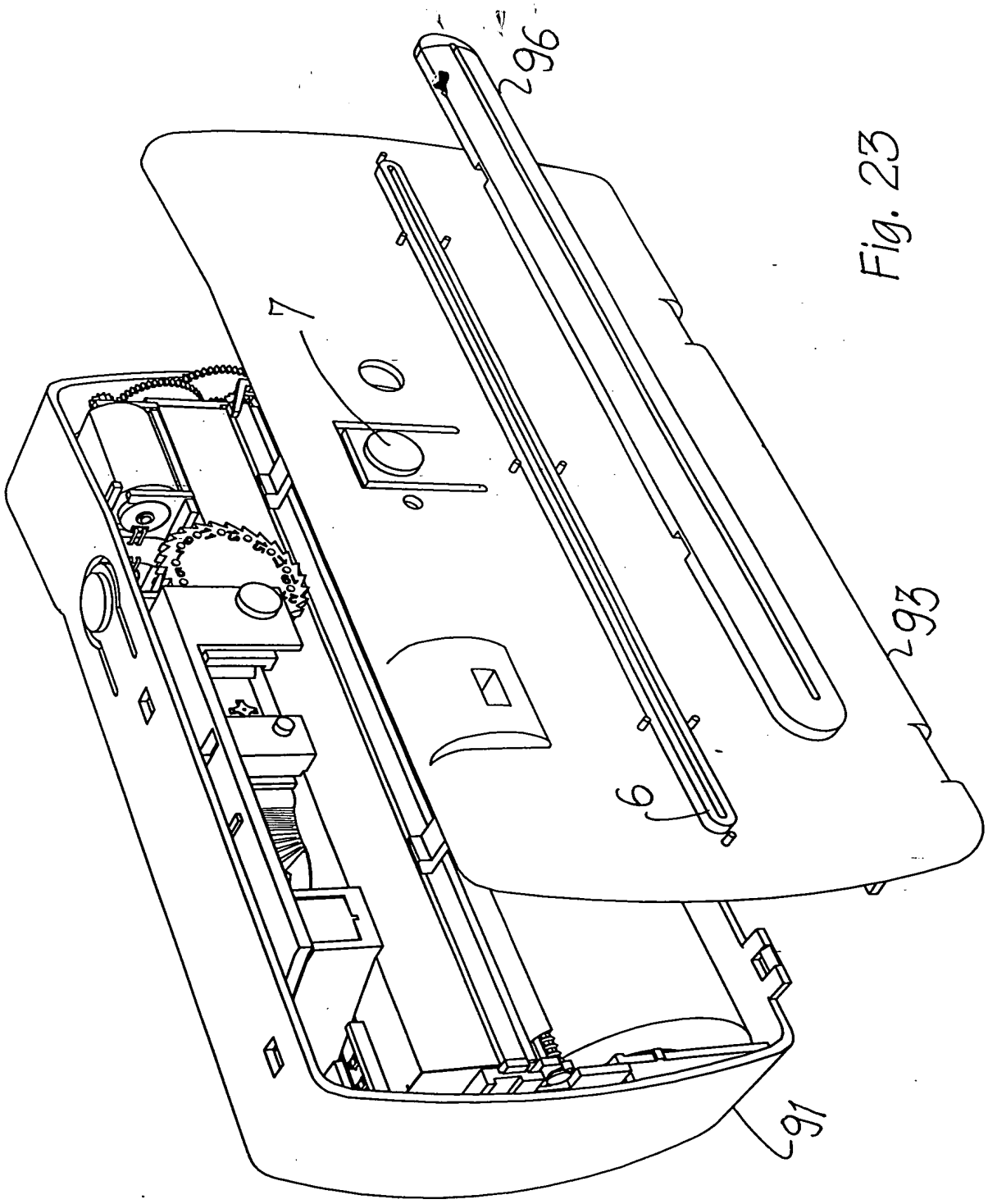

Fig. 23

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